



Highly sensitive negative resist for the production of integrated circuits

AR-N 4340 產品說明

AR-N 4340 為化學增幅型,高敏感度負型光阻,適合各類次微米(sub-um)電路運用。 產品基本資料如下:

| | | AR-N 4340 |
|--------------------------------|------|-----------|
| Film thickness@4000rpm | um | 1.4 |
| Viscosity 25°C | mPas | 18 |
| Resolution | um | 0.5 |
| Contrast | | 5.0 |
| Flash point | °C | 44 |
| Storage 6 months ¹ | °C | 10 - 18 |
| Production status ² | | routine |

1 Product is guaranteed 6 months shelf life from the data of sale if stored correctly. 在正確的儲存條件下,產品保證的有效期為銷售日起6個月 Product can also be used without guarantee until the date indicated on the label 在無提供保證的情況下,產品可使用至標籤上所示的有效期

2 Production status:

on-demand:產品無固定排程生產,需先詢問價格。可能會有最小量訂單,或需

等待批次生產排程。

Routine: 產品固定排程生產,交貨期約2-4週。

產品包裝:

其它包裝可依客戶需求增加.

出貨:

✓ 2-4週。德國運出。✓ 1週。國內庫存。 (本產品目前暫無國內庫存)

◎ AR-N 4340 GHS 標識





Highly sensitive negative resist for the production of integrated circuits

Characterization 產品特性

- i-line, g-line 曝光波長: i-line (365nm), g-line (436nm)
- highest sensitivity, excellent resolution 高敏感度,高解析度.
- good adhesion, high contrast, chemically enhanced 化學增幅型,具高對比,與基板黏著度良好.
- undercut profiles (lift-off) are possible
 可調整成下切型圖案,用於懸浮剝離製程.
- plasma etching resistant 耐各類電漿蝕刻.
- temperature-stable up to 220 °C after subsequent treatment 調整適合製程可使光阻在220°C高溫維持穩定.
- novolac with photochemical acid generator and amine-based crosslinking agent 成份含酚醛樹酯,光酸與架橋劑.
- Safer solvent PGMEA 使用較安全溶劑丙二醇單甲醚醋酸酯





Highly sensitive negative resist for the production of integrated circuits

| Property I | | | |
|------------------------|-------|-----------|--|
| Parameter | | AR-N 4340 | |
| Solids content | % | 32 | |
| Viscosity@25°C | mPa.s | 18 | |
| Film thickness@4000rpm | um | 1.4 | |
| Resolution | um | 0.5 | |
| Contrast | | 5.0 | |
| Flash point | °C | 42 | |
| Storage 6 months | °C | 10 - 18 | |

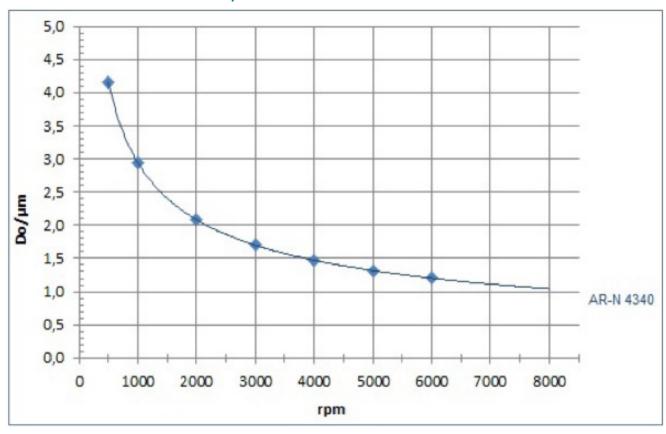
| Property II | | | | |
|---|----------------|---------------------------------------|---------|--|
| Glass trans. temperature | °C | 102 | | |
| Dielectric constant | | 3.1 | | |
| Cauchy coefficients | | unexposed | exposed | |
| | N_0 | 1.593 | 1.599 | |
| | N_1 | 75.4 | 81.4 | |
| | N ₂ | 80.0 | 81.4 | |
| Plasma etching rate 5 Pa, 240-250 V Bias | nm/min | Ar-sputtering | 8 | |
| | | O ₂ | 173 | |
| | | CF ₄ | 33 | |
| | | 80 CF ₄ +16 O ₂ | 93 | |





Highly sensitive negative resist for the production of integrated circuits

Spin curve of AR-N 4340

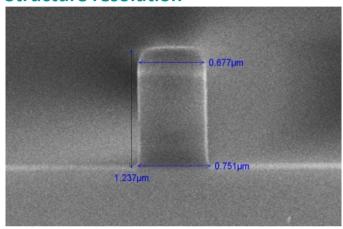




Highly sensitive negative resist for the production of integrated circuits



Structure resolution



AR-N 4340 Film thickness 1.4 μm Resist structure 0.7 μm L/S

Process parameter

| Substrate | Si 4" wafer | |
|-------------|----------------------------|--|
| Soft-bake | 85 °C x 60 sec, hot plate | |
| Exposure | i-line stepper (NA: 0.65) | |
| Development | AR 300-475 x 60 sec, 22 °C | |

Resist structures



AR-N 4340 Film thickness 2.0 μm Resist structure 4.0 μm

Process chemicals

| Adhesion promoter | AR 300-80 |
|-------------------|----------------------|
| Developer | AR 300-475 |
| Thinner | AR 300-12 |
| Remover | AR 300-76, AR 300-72 |





Highly sensitive negative resist for the production of integrated circuits

This diagram shows exemplary process steps for resist AR-N 4340. All specifications are guideline values which must be adapted to own specific conditions. For further information on processing, 2 "Detailed instructions for optimum processing of photoresists". For recommendations on wastewater treatment and general safety instructions, 2 "General product information on Allresist photoresists".

圖示AR-N 4340產品製程參數的範例. 所有參數為參考值,使用者應依設備環境實際狀況加以調整

| Coating | | AR-N 4340 | |
|------------------------------------|--------------------|---|--|
| Coating | | 1.4 um@4000rpm x 60 sec | |
| Caft halia (14 00) | | 90°C x 1 min hot plate, or | |
| Soft bake (±1 °C) | 1111111111111111 | 85°C x 25 min convection oven | |
| UV exposure | | Broadband UV, 365nm, 405nm, 436nm | |
| O v exposure | | Exposure dose (E $_0$ broadband UV stepper):140 mJ/cm 2 ,1.4 μ m | |
| Cross-linking bake | | 95°C x 2 min hot plate, or | |
| (±1°C) | 111111111111111111 | 90°C x 25 min convection oven | |
| Development | Millill | AR 300-475, 60 sec | |
| (21-23±0.5°C) puddle | | Note: By extending the development time, an undercut (lift-off) of the resist structure can be obtained at minimum possible exposure dose | |
| Rinse | | DI water, 30 sec | |
| Hardening of structures (optional) | s up to 300°C | Flood exposure 150 mJ/cm², bake 115°C, 1 min hot plate | |
| Customer specific technology | iff iff iff | Generation of semiconductor property or lift-off | |
| Removal | | AR 300-76 or O ₂ plasma ashing | |







Reference data for process tuning

TCD vs. bake temperature

| Temperature °C | TCD (s) | Dose (mJ/cm²) |
|----------------|---------|---------------|
| 70 | 20 | 480 |
| 80 | 22 | 250 |
| 90 | 24 | 140 |
| 100 | 41 | 65 |
| 110 | 80 | 55 |
| 120 | 210 | 220 |
| 130 | ∞ | ∞ |

Development recommendations

| Developer | AR 300-26 | AR 300-26 | AR 300-26 |
|-----------|-----------|-----------|------------|
| AR-N 4340 | 1:1 | undilute | AR 300-475 |

Samples were dried at 85 °C and crosslinked at temperatures as indicated (developer: AR 300-475). The development strongly depends on the bake temperature.

Above a temperature of 130 $^{\circ}$ C, resist AR-N 4340 is not developable anymore. Optimum temperatures range between 90 and 100 $^{\circ}$ C.

樣品條件: 軟烤 85℃, 以表列溫度進行曝後考.

顯影劑: AR 300-475

顯影時間與曝後烤溫度有直接關係.

曝後烤溫度達130℃, 光阻就無法被顯開.

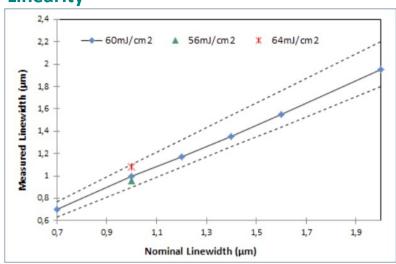
最適合曝後烤溫度介於90℃至100℃.



Highly sensitive negative resist for the production of integrated circuits

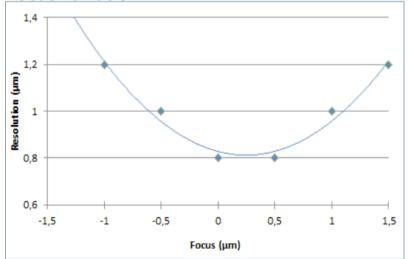


Linearity



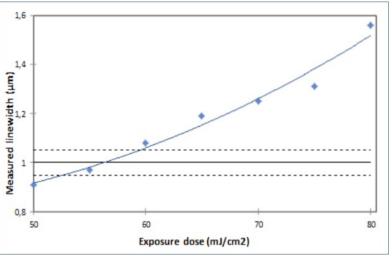
Up to a line width of 0.7 $\mu m,$ the linearity is in the desired range (parameter see grafic Focus variation)

Focus variation



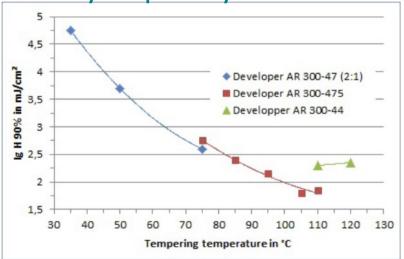
The resist achieves a resolution of 0.8 μ m optimal focus adjustment REM measurement: Thickness 1,5 μ m, PEB 105 °C, 180 s, I-line stepper (NA: 0,65), Developer AR 300-475.

Optimum exposure dose



The optimum exposure dose for 1 μ m-bars is 56 mJ/cm2 (parameter see grafic Focus variation)

Sensitivity in dependency on the bake



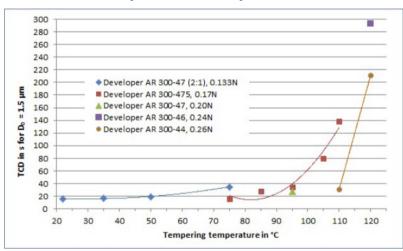
Samples were both dried and crosslinked at temperatures as indicated. The optimum working range is between 90 and 110 °C.



Highly sensitive negative resist for the production of integrated circuits

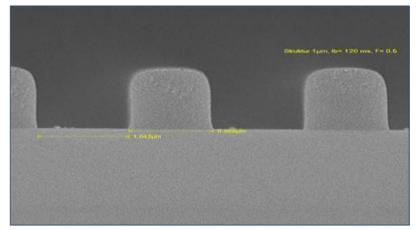


Time for complete development vs. bake



The time for complete development is very short at bake temperatures of < $50\,^{\circ}$ C, even if weak developers are used. With increasing temperature, the time for complete development (TCD) is considerably prolonged. Above a temperature of $120\,^{\circ}$ C, complete development of the resist is no longer possible.

Temperature stability after hardening



Hardened resist bar structures after tempering at 200 °C

The developed structures are stable between 140 -160 °C, depending on the drying procedure (hot plate or oven). Structures can be stabilized up to temperatures of 220 °C by flood exposure and a subsequent bake at 120 °C.





Highly sensitive negative resist for the production of integrated circuits

